FIG. 1

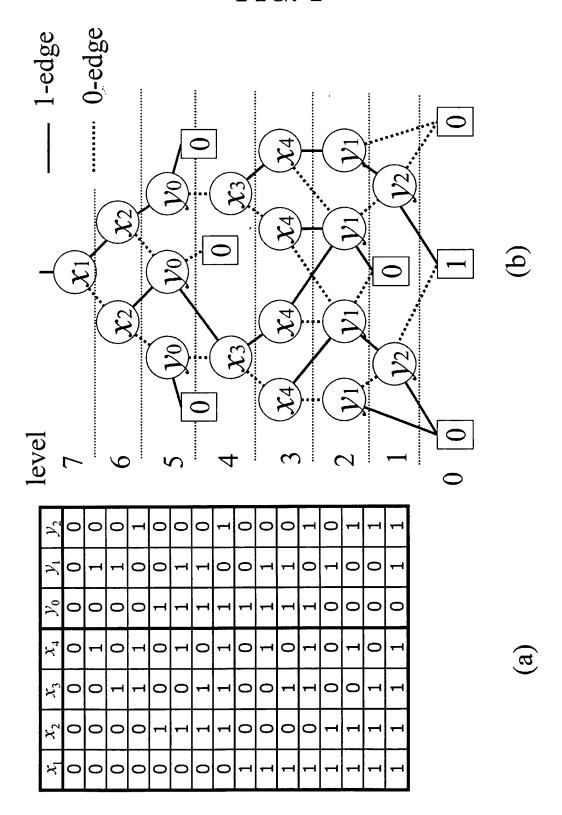


FIG. 2

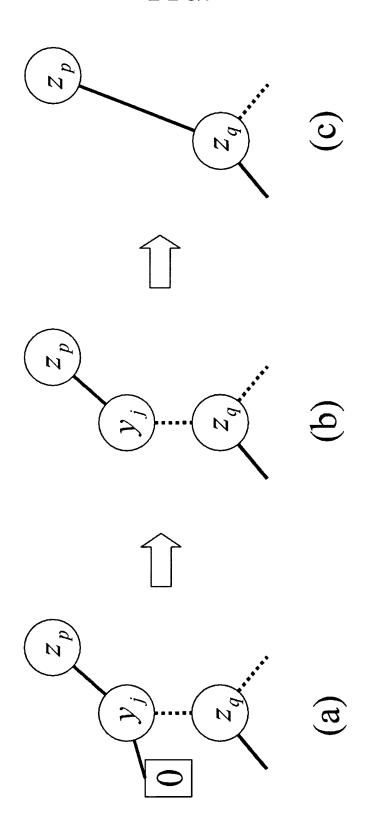


FIG. 3

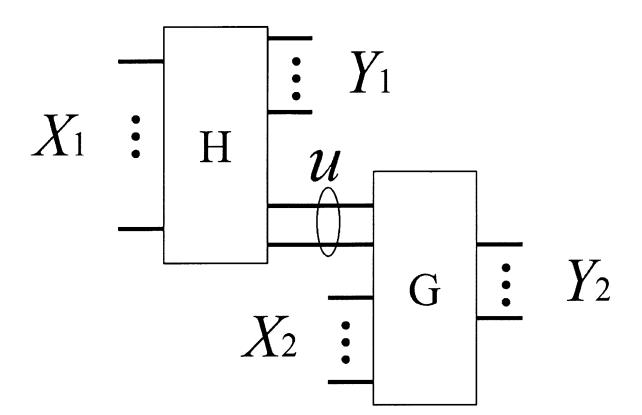


FIG. 4

Nidth 2 4 2 2 2 2 1

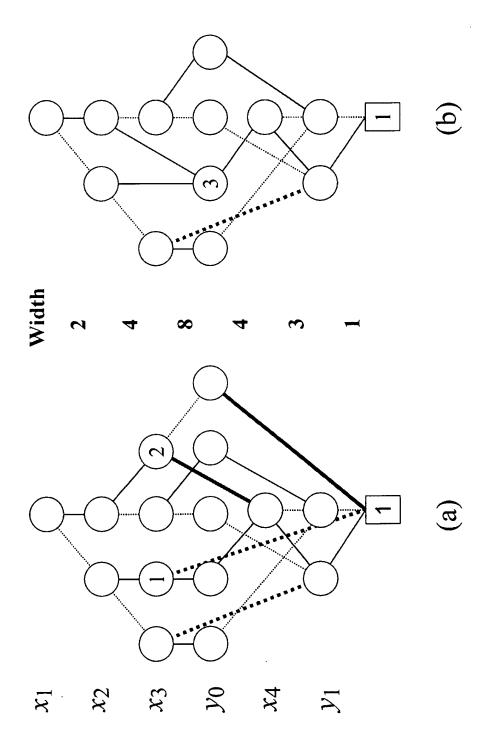


FIG. 5

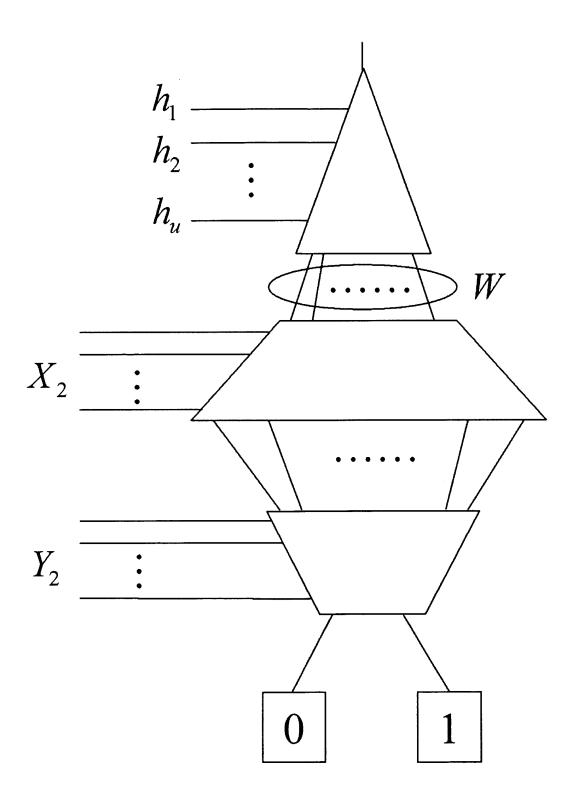


FIG. 6

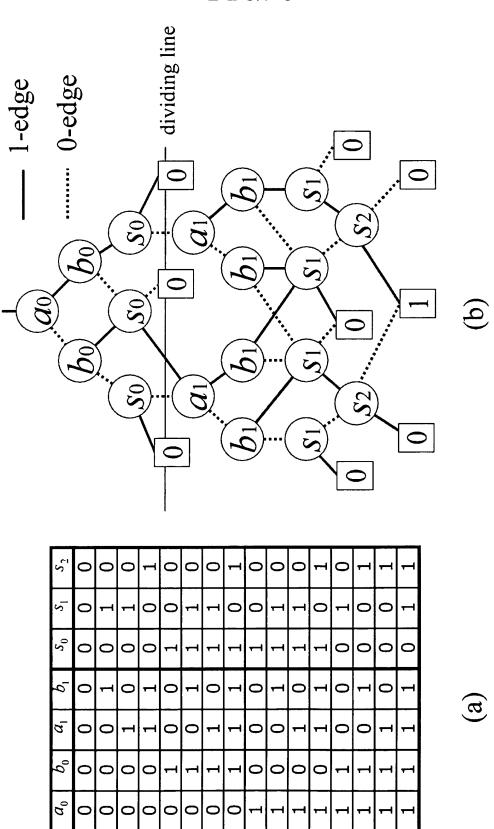
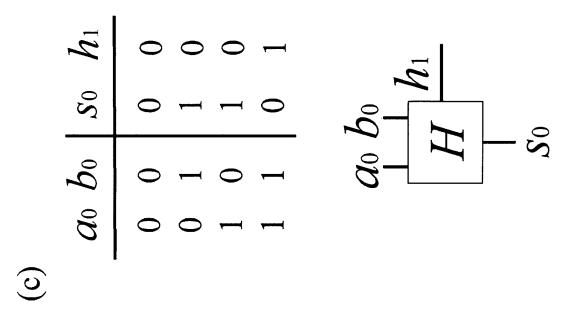


FIG. 7



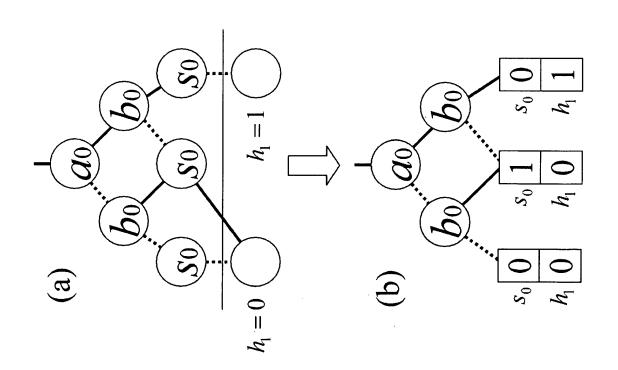


FIG. 8

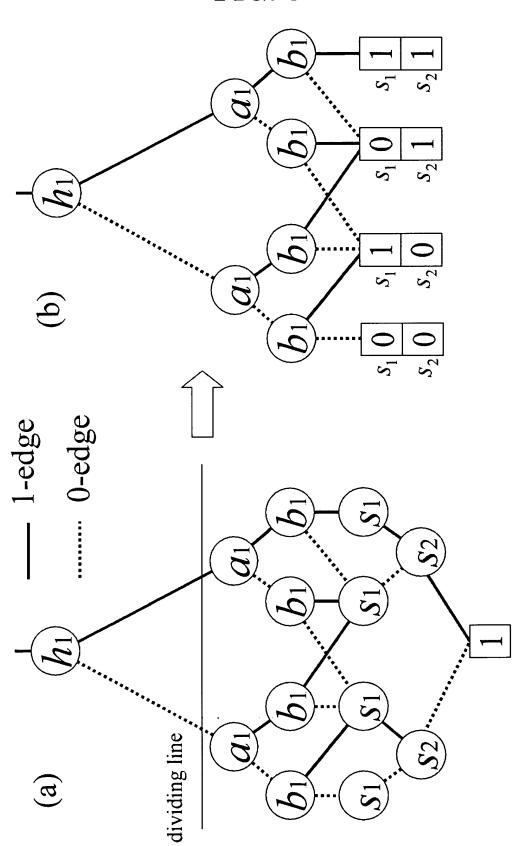
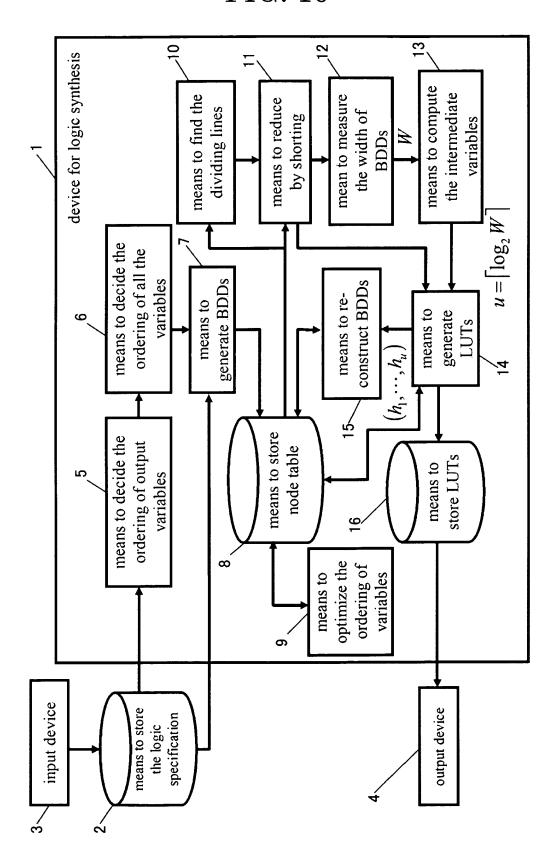


FIG. 9

(q)		$egin{array}{cccccccccccccccccccccccccccccccccccc$				S_0 S_1 S_2			Number of stages:
	\$2	0	0	0	_	0		 -	—
	S_1	0	-		0	-	0	0	
'	b_1	0		0		0		0	
	a_1	0	0			0	0		
	h_1	0	0	0	0			—	-

(a)

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FIG. 11

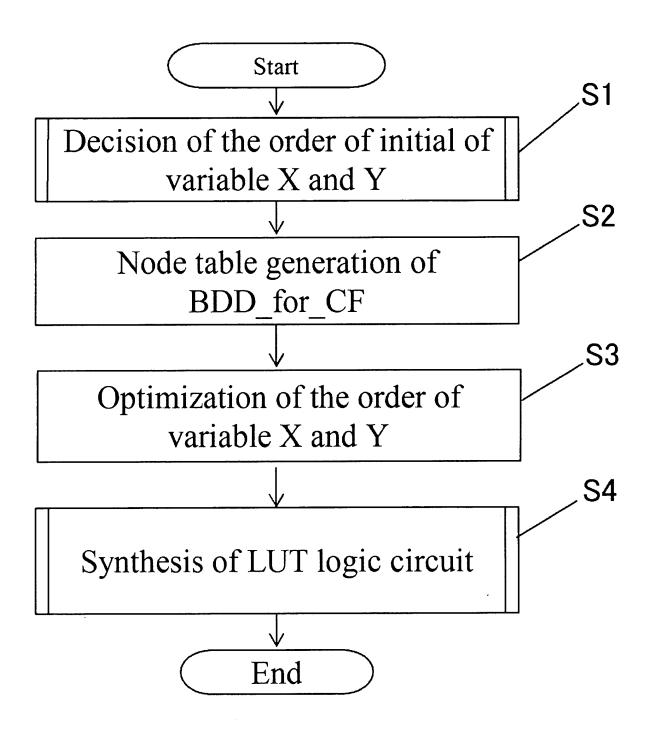
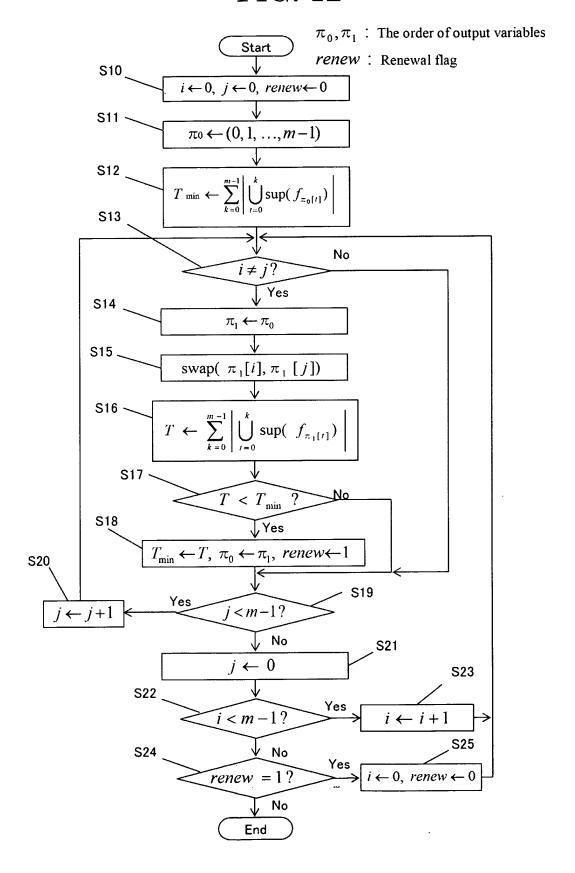


FIG. 12



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FIG. 13 Structure of node table

Address		
0000	labels of variable	
	Address of child node of 0-edge	One node
	Address of child node of 1-edge	
0001	labels of variable	
	Address of child node of 0-edge	
	Address of child node of 1-edge	
0002	labels of variable	
	Address of child node of 0-edge	
	Address of child node of 1-edge	
•	•	•

FIG. 14

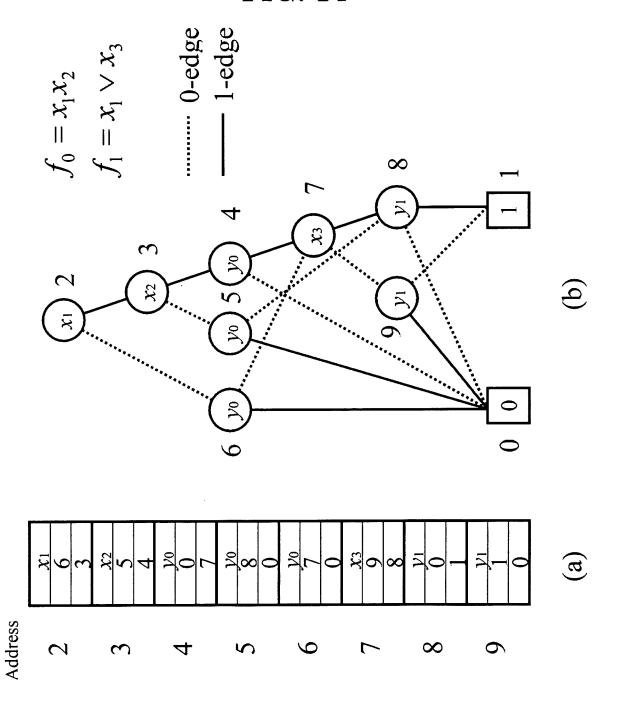


FIG. 15

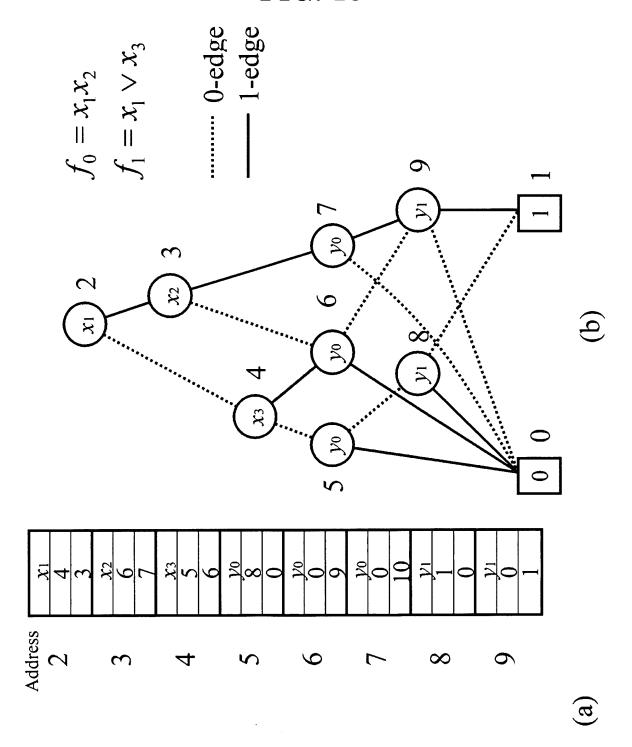
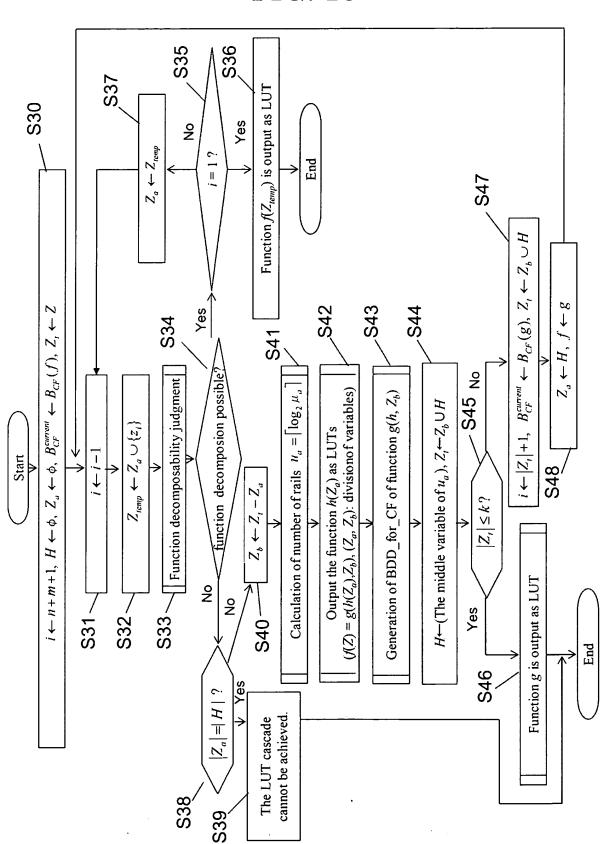


FIG. 16



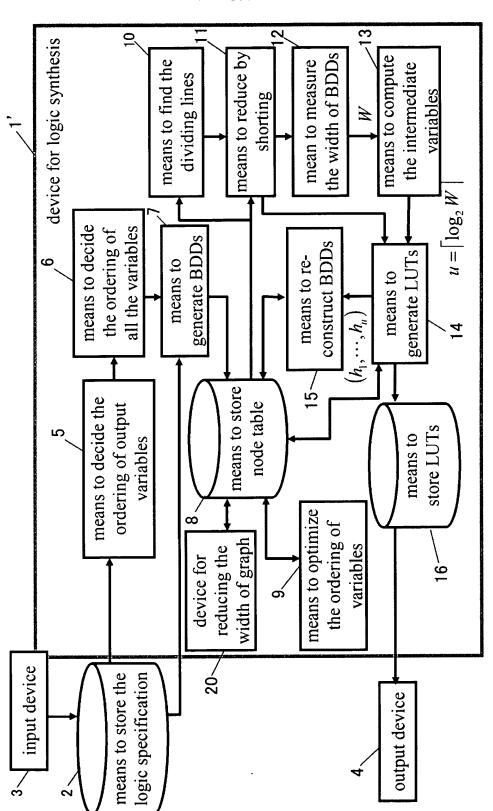
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FIG. 17

	S						
Mishchenko [15]	Cas	30	5	18	4	2	3
	Number of stages	18	10	18	3	9	4
	LUT	3015	213	728	51	37	47
method of present invention	Cas	5	Ţ	4	Ī	Ι	Π
	Number of stages	11	12	18	4	8	9
	LUT	320	115	385	46	20	54
Out		25	45	66	59	10	21
In		33	45	135	22	34	24
Name		C1908	apex1	apex6	duke2	term1	ttt2

Note : Number of input k of LUT as k=10.

FIG. 18



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FIG. 19

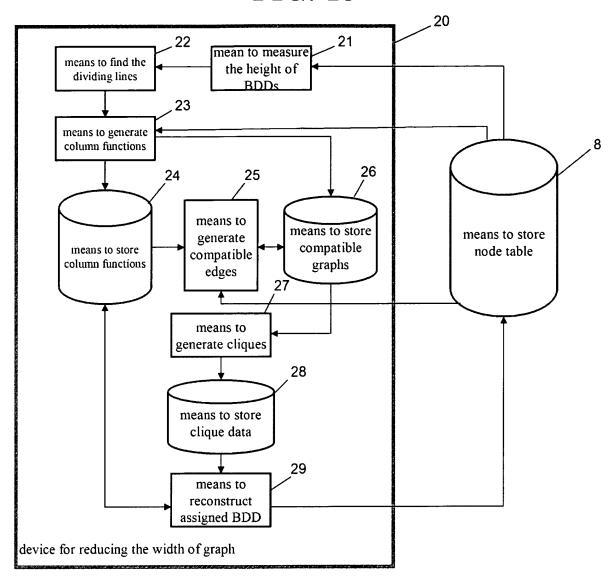


FIG. 20

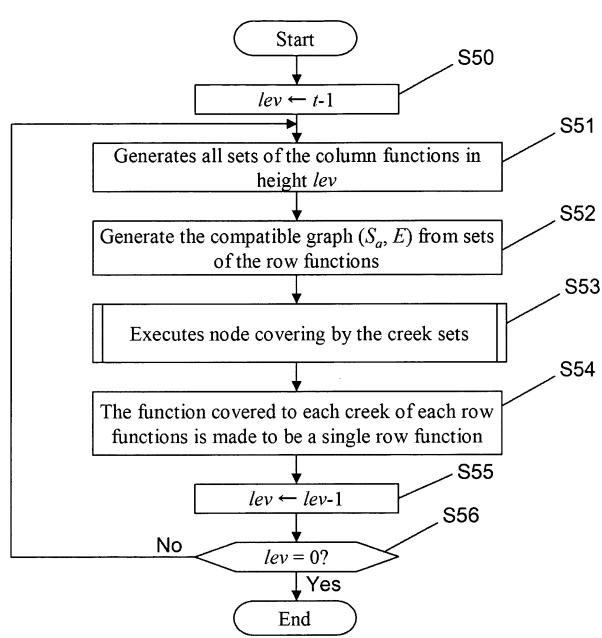


FIG. 21

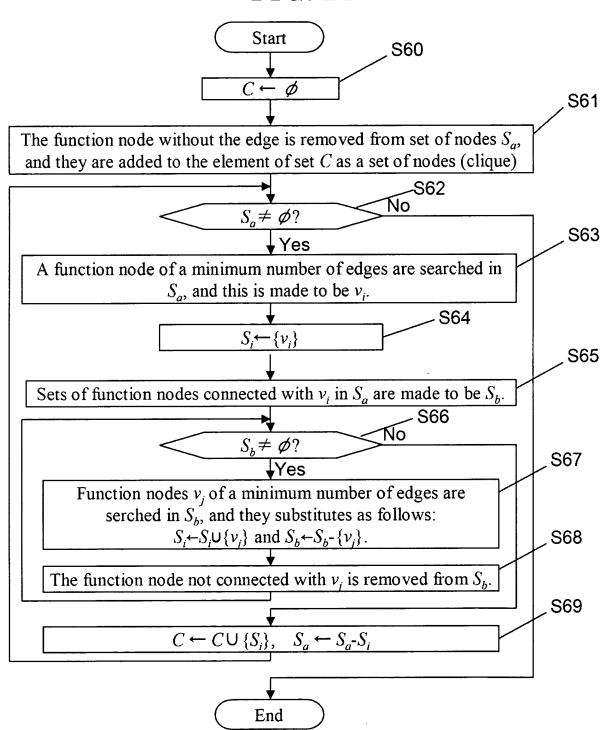
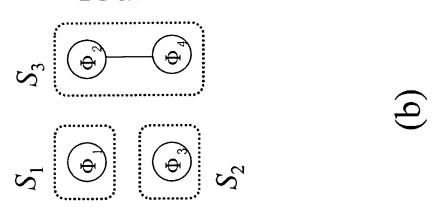


FIG. 22



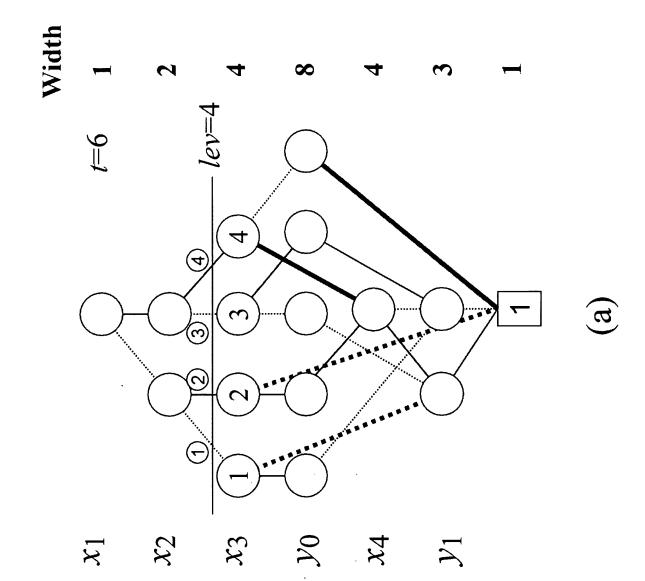
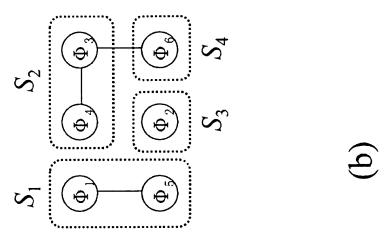
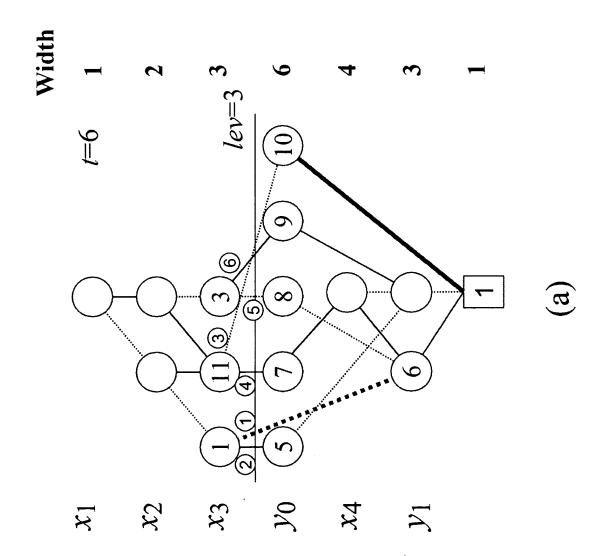


FIG. 23





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FIG. 24

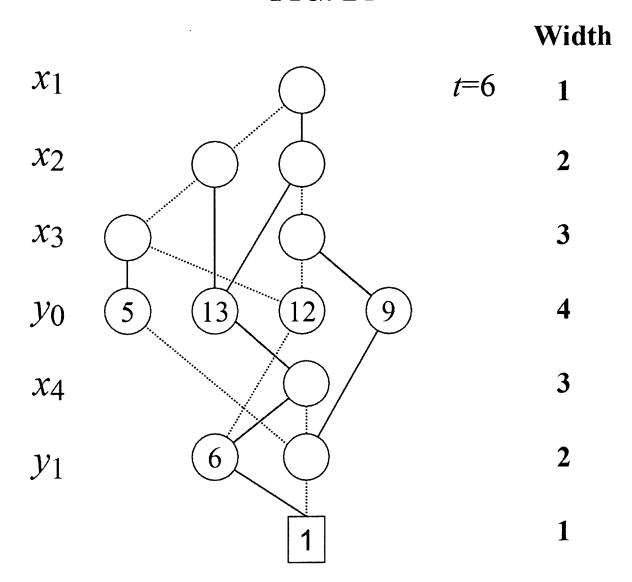


FIG. 25

